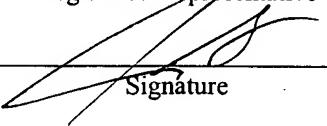


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Name of Applicant, Assignee or
Registered Representative


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Technology Center 2100

Patent
Our Case No. 10519-31

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

| | | |
|-----------------------|--|--------------------------|
| In re Application of: |) | |
| Kleveland et al. |) | |
| Serial No.: | 10/024,646 |) |
| Filed: | December 14, 2001 |) |
| For: | Memory Device and Method for Redundancy/Self-Repair |) |
| |) | Examiner: J. Tabone, Jr. |
| |) | Group Art Unit: 2133 |

SECOND SUPPLEMENTAL
INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

Pursuant to the obligation under 37 C.F.R. § 1.56 and in conformance with 37 C.F.R. §§ 1.97-1.99, Applicants hereby submit documents A1-A16 listed on the attached form PTO-1449 for consideration by the Examiner. Copies of documents, as required, are enclosed herewith. Applicants request that the Examiner review the entire disclosure of these documents and make them of record.

10/22/2004 FNTEK11 00000010 10024646

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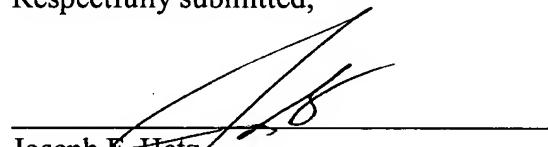
180.00 OP

The filing of this Information Disclosure Statement does not constitute an admission that the information cited herein is, or is considered to be, material to patentability as defined in 37 C.F.R. § 1.56(b). Further, Applicants reserve the right to contest that any of the information submitted herewith is prior art against the present application.

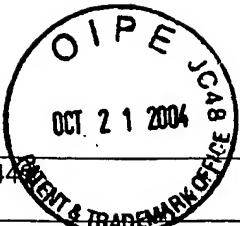
Because this Information Disclosure Statement is being filed after the mailing date of the first Office Action but prior to the mailing of the final action or notice of allowance, Applicants submit herewith the fee set forth in 37 C.F.R. § 1.17(p)

Dated: October 19, 2004

Respectfully submitted,


Joseph F. Hetz
Reg. No. 41,070
Attorney for Applicants

BRINKS HOFER
GILSON & LIONE
P.O. Box 10395
Chicago, Illinois 60610
(312) 321-4719



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|---|--------------------------|------------------------|
| FORM PTO-144 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (use several sheets if necessary) | SERIAL NO. 10/024,646 | CASE NO. 10519-31 |
| | FILING DATE 12/14/01 | GROUP ART UNIT 2133 |
| APPLICANT(S): Kleveland et al. | | |

REFERENCE DESIGNATION **U.S. PATENT DOCUMENTS**

| EXAMINER INITIAL | DOCUMENT NUMBER Number-Kind Code (if known) | DATE | NAME | CLASS/ SUBCLASS | FILING DATE |
|------------------|--|---------|--------------------|------------------------|----------------|
| A1 | 6,420,215 B1 | 07/2002 | Knall et al. | | |
| A2 | 6,515,923 B1 | 02/2003 | Cleeves | OCT 26 2004 | |
| A3 | 6,525,953 | 02/2003 | Johnson | | |
| A4 | 6,567,287 B2 | 05/2003 | Scheuerlein | Technology Center 2100 | |
| A5 | US 2002/0028541 A1 | 03/2002 | Lee et al. | | |
| A6 | US 2003/0115514 A1 | 06/2003 | Iilkbahar et al. | | |
| A7 | US 2003/0115518 A1 | 06/2003 | Kleveland et al. | | |
| A8 | 6,658,438 | 12/2003 | Moore et al. | | |
| A9 | 6,661,730 | 12/2003 | Scheuerlein et al. | | |

| EXAMINER INITIAL | OTHER ART – NON PATENT LITERATURE DOCUMENTS (Include name of author, title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date page(s), volume-issue number(s), publisher, city and/or country where published.) | |
|------------------|--|--|
| A10 | "A 16Mb Mask ROM with Programmable Redundancy," Nsruka et al., ISSCC 1989/Session 10: Nonvolatile Memories/Paper THAM 10.1, 2 pages, February 16, 1989. | |
| A11 | "Circuit Technologies for 16Mb DRAMs," Mano et al., ISSCC 1987/Session I: MEGABIT DRAMs/Paper WAM 1.6, 2 pages, February 27, 1987. | |
| A12 | "Memory Device and Method for Storing and Reading Data in a Write-Once Memory Array," U.S. Patent Application Serial No. 09/877,720, filed June 8, 2001; inventors: Christopher S. Moore, James E. Schneider, J. James Tringali and Roger W. March. | |
| A13 | "Memory Device and Method for Storing and Reading a File System Structure in a Write-Once Memory Array," U.S. Patent Application Serial No. 09/877,719, filed June 8, 2001; inventors: Christopher S. Moore, James E. Schneider, J. James Tringali and Roger W. March. | |
| A14 | "Method for Reading Data in a Write-Once Memory Device Using a Write-Many File System," U.S. Patent Application Serial No. 09/818,138, filed June 8, 2001; inventors: J. James Tringali, Christopher S. Moore, Roger W. March, James E. Schneider, Derek Bosch, and Daniel C. Steere. | |
| A15 | "Method for Re-Directing Data Traffic in a Write-Once Memory Device," U.S. Patent Application Serial No. 09/877,691, filed June 8, 2001; inventors: J. James Tringali, Christopher S. Moore, Roger W. March, James E. Schneider, Derek Bosch, and Daniel C. Steere. | |
| A16 | "Method for Making a Write-Once Memory Device Read Compatible with a Write-Many File System," U.S. Patent Application Serial No. 10/023,468, filed December 14, 2001; inventors: Christopher S. Moore, Matt Fruin, Colm Lysaght, and Roy E. Scheuerlein. | |

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| EXAMINER | DATE CONSIDERED |
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609;
 Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.